

# ELEKTRONIKA II

Zapiski z avditornih vaj

3

TK

Šolsko leto 2008/2009  
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Avtor dokumenta Vesna Koderman  
Skeniranje Simon Kovše



## UREJANJE DOKUMENTA

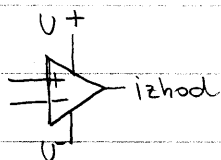
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| VERZIJA | 01.01      |
| DATUM   | 10.01.2010 |

## OPOMBE

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# ELEKTRONIKA - 2. del :

- ~ operacijski ojačevalnik
- ~ vezja z op. oji.
- ~ frekvenčna karakteristika op. oji.
- ~ lastnosti op. oji.

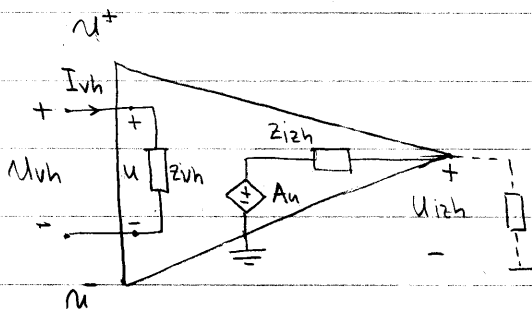


• SIMBOL ZA OP. OJ. :

↳ sestava, odšteva, integrira analogne signale ... itd.

• Lastnosti, ki jih zahtevamo :

$Z_{vh}$ ,  $Z_{izh}$ ,  $A$



• Idealni op. oji

- PREDPOSTAVIMO:  $Z_{vh} = \infty$ ,  $Z_{izh} = 0 \Rightarrow A \rightarrow \infty$

→ POSLEDICE:  $I_{vh} = 0$ ;  $U_{izh} = A \cdot U_{vh} = A(U^+ - U^-)$ ;  $U_{vh} = 0 \Rightarrow U^+ = U^-$

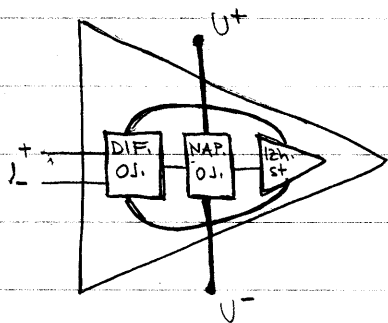
to stvar ni res, ampak predpostavimo, da gre za tako majhne napetosti, da so kar približno enake...

? DIFERENČNI

DIFERENCIALNI OJ. je poseben gradnik,

ki ga predno srečamo na vnosu ... diferencna med vhodnima napetostima dobro ojača.

Ponavadi je tri stopenjski ... na ta način zadostimo zgornjim zahtevam ...

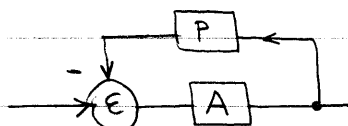


• DIF. VHOD :

• INTERVIRAJOČI OJ. :

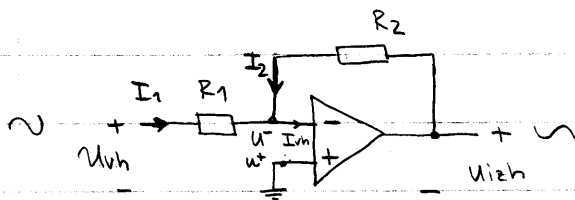
• NEINTERVIRAJOČI OJ. :

• POUKATNA VEZAVA :



↳ na ta način dosežemo boljše lastnosti!

## INTERVIRAJOČ OJAČEVALNIK:



⇒ imamo povratno zanko:

$$R_1 = 1\text{ k}\Omega ; R_2 = 9\text{ k}\Omega$$

$$I_{vh} = 0 \text{ (to je vedno tako!)}^?$$

⇒ Na vnosu imamo nek signal majhne napetosti, katerega ojačevalnik invertira fazo...

$$U^+ = U^- = 0$$

... pomeni, da sta oba na masi

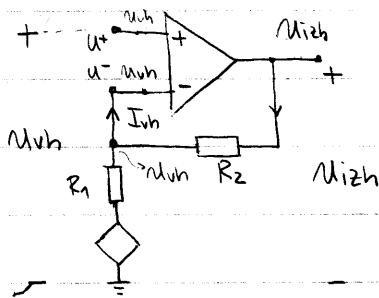
$$I_1 + I_2 = I_{vh} \Rightarrow \frac{U_{vh}}{R_1} + \frac{U_{izh}}{R_2} = 0 \text{ ... ker je } I_{vh} = 0$$

$$\Rightarrow U_{izh} = -\frac{R_2}{R_1} U_{vh}$$

ojačanje je odvisno od razmerja uporov

$$\Rightarrow U_{izh} = -10 U_{vh}$$

## NEINVERTIRAJOČ OJAČEVALNIK:



$$I_{vh} = 0$$

$$U^+ = U^- = U_{vh}$$

lahko zapišemo kot napetostni delilnik:

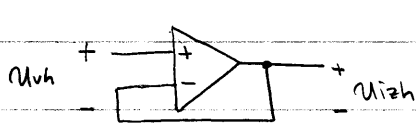
$$\frac{U_{izh} - U_{vh}}{R_2} = \frac{U_{vh}}{R_1}$$

$$\Rightarrow U_{izh} = \left(1 + \frac{R_2}{R_1}\right) \cdot U_{vh}$$

$$R_2 = 9\text{ k}\Omega, R_1 = 1\text{ k}\Omega \Rightarrow U_{izh} = 10 U_{vh}$$

! Povratne vezave moramo vedno risati v negativno smer ...

$$R_2 \rightarrow 0, R_1 \rightarrow \infty : U_{izh} = U_{vh}$$

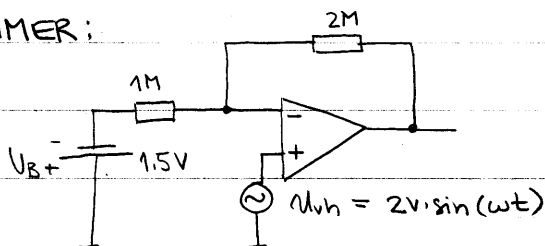


NAPETOSTNI SLEDILNIK :

- ojačanje ima vedno 1

$$-U_B \frac{R_2}{R_1} + U_{vh} \left(1 + \frac{R_2}{R_1}\right)$$

PRIMER:



- enosmerni vir bomo obravnavali kot invertirajoč ojačevalnik, izmenični vir pa kot neinvertirajoči.

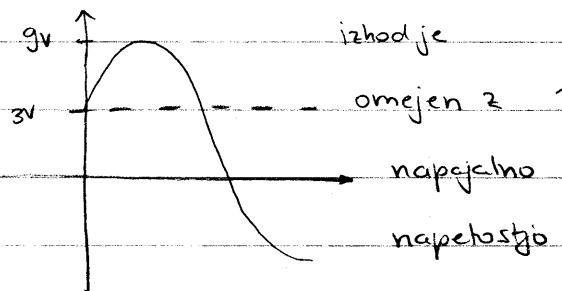
Izhodna napetost bo vsota obeh

1.)  $U_B = 0$  ... neinvertirajoč o.j.

$$\Rightarrow U_{izh_1} = \left(1 + \frac{2}{1}\right) \cdot 2V \cdot \sin(\omega t) = 6V \cdot \sin(\omega t)$$

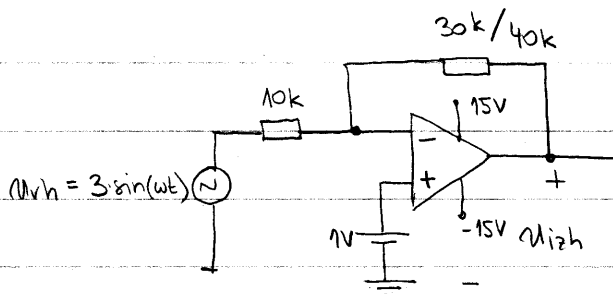
2.)  $U_B$  ;  $U_{vh} = 0$  ... invertirajoč o.j.

$$\Rightarrow U_{izh} = -\frac{2}{1} (-1.5) = 3V$$



ODGovor :  $U_{izh} = 3V + 6V \sin(\omega t)$

PRIMER:



$U_{vh} = 0$

$$30k : U_{izh_1} = \left(1 + \frac{30}{10}\right) \cdot 1V = 4V$$

$$40k : U_{izh_1} = \left(1 + \frac{40}{10}\right) \cdot 1V = 5V$$

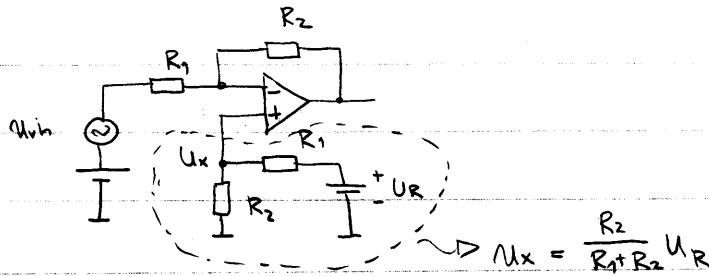
$U_B = 0$  :

$$30k : U_{izh} = -\frac{30}{10} \cdot 3 \sin(\omega t) = -9V \cdot \sin(\omega t)$$

$$40k : U_{izh} = -\frac{40}{10} \cdot 3 \sin(\omega t) = -12V \cdot \sin(\omega t)$$

⇒ 30k :  $U_{izh} = 4V - 9V \cdot \sin(\omega t)$  ; min: -5V , max: 13V

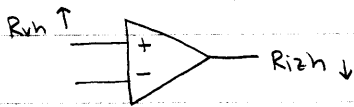
⇒ 40k :  $U_{izh} = 5V - 12V \cdot \sin(\omega t)$  ; min: -7V , max: 17V



$U_x = \frac{R_2}{R_1 + R_2} U_R$

⇒  $U_{izh} = - \underbrace{\frac{R_2}{R_1} (U_{vh} + U_x)}_{\text{v minus spantki}} + (1 + \frac{R_2}{R_1}) (\frac{R_2}{R_1 + R_2}) U_R$

4.3.2009



⇒ Ojačanje je tako veliko, da ga obravnavamo kot neskončno :  $A_u \rightarrow \infty$

⇒  $R_{vh} \uparrow$  (visoka)  $\rightarrow R_{vh} \rightarrow \infty$

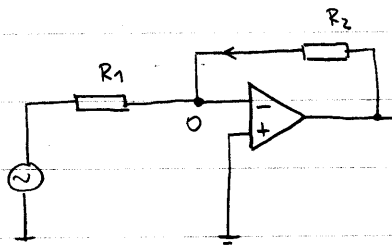
$R_{izh} \downarrow$  (nižja) ... to pomeni, da mora

biti  $i_{vh} = 0$

⇒  $R_{izh} \rightarrow 0$  ;  $U_{izh} = A \cdot U_{vh}$  pri  $A \rightarrow \infty$

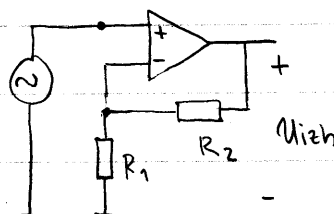
⇒  $U_{vh} = 0$  ;  $u^+ = u^-$

• INVERTIRAJČE OJ. :



$A_u = -R_2/R_1$

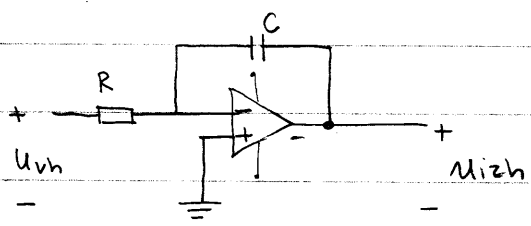
• NEINVERTIRAJČE OJ. :



$A_u = (1 + \frac{R_2}{R_1})$

• Kaj se zgodi, če namesto uporov damo noter kondenzator ...  $\Rightarrow$  premaknemo še v frekvenčni prostor

• Kako bi napisali napetostno ojačanje, če vezje izgleda takole:



$\Rightarrow$  To je INTEGRATOR ... v čas. prostoru integrira signal

$$(A_u = -R_2/R_1) \rightsquigarrow A_u = -\frac{Z_2}{Z_1}$$

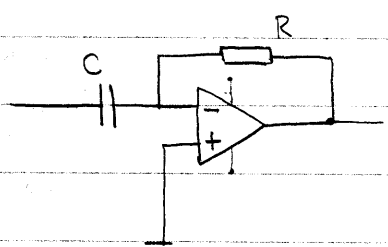
$$A_u = -\frac{1}{j\omega C R}$$

$$= \frac{U_{izh}}{U_{vh}} \Rightarrow U_{izh} = -\frac{1}{j\omega R C} \cdot U_{vh}$$

namesto upornosti imamo impedanco

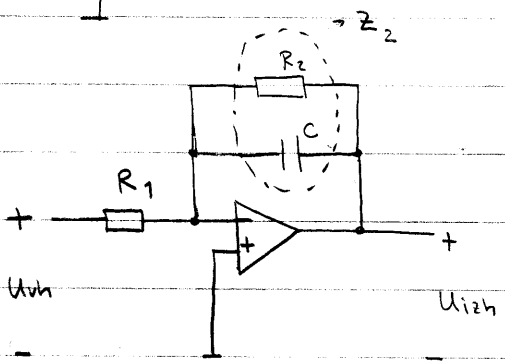
• Kaj se zgodi, če zamenjam upor in kondenzator?

$\rightarrow$  DORIMO DIFERENCIATOR (v časovnem prostoru odvaja signal)



$$A_u = -\frac{R}{1/j\omega C} = -j\omega R C$$

PRIMER:



$$Z_2 = \left( \frac{1}{R_2} + j\omega C \right)^{-1} = \frac{1}{\left( \frac{1 + j\omega C R_2}{R_2} \right)} = \frac{R_2}{1 + j\omega C R_2}$$

integrator

$$\Rightarrow A_u = -Z_2/Z_1 = -\frac{R_2/R_1}{1 + j\omega C R_2}$$

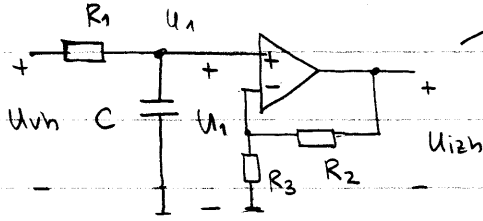
$$A_u = -Z_2/Z_1 = -\frac{R_2/R_1}{1 + j\omega C R_2}$$

$$= -\frac{R_2}{R_1} \cdot \frac{1}{j\omega C R_2} = \omega_p^{-1}$$

$$\Rightarrow \omega_p = \frac{1}{R_2 C}$$

$$Z_2 = \left( \frac{1}{j\omega C} + \frac{1}{R_2} \right)^{-1} = \frac{R_2 + j\omega C R_2^2}{j\omega C R_2} \cdot \left( \frac{j\omega C R_2 + 1}{R_2} \right)^{-1} = \frac{R_2}{1 + j\omega C R_2}$$

PRIMER:



→ Diferenciator:

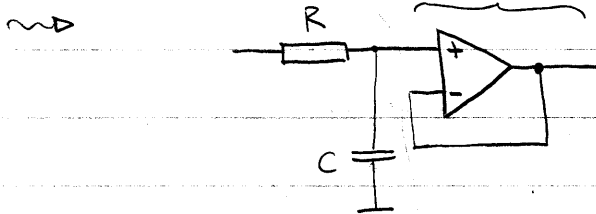
$$= \frac{U_{izh}}{U_1} = 1 + \frac{R_2}{R_3}$$

$$U_1 = U_{vh} \frac{1/j\omega C}{R_1 + 1/j\omega C} = U_{vh} \frac{1}{R_1 j\omega C + 1}$$

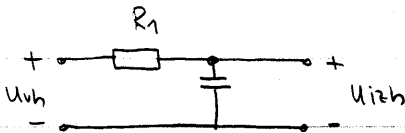
$$A_u = \left(1 + \frac{R_2}{R_3}\right) \frac{1}{j\omega C R_1 + 1}$$

$$U_{izh} = U_1 \left(1 + \frac{R_2}{R_3}\right) = \frac{U_{vh}}{1 + R_1 j\omega C} \left(1 + \frac{R_2}{R_3}\right)$$

$$A_u = \left(1 + \frac{R_2}{R_3}\right) = 1$$



$R_2 \rightarrow 0$  ;  $R_3 \rightarrow \infty$



nizkopasovno sito:

pri visokih f. je kondenzator

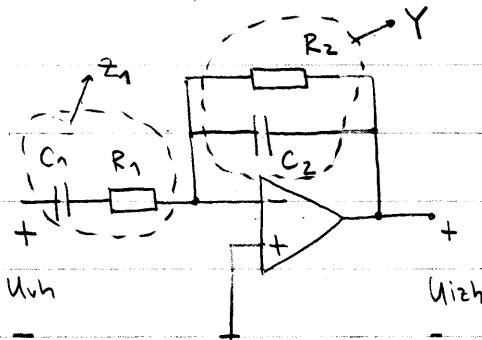
kratak stik, pri nizkih f.

pa odpre sponke.

$$z_1 = \frac{1}{j\omega C_1} + R_1 = \frac{1 + j\omega C_1 R_1}{j\omega C_1}$$

$$z_2 = \left(\frac{1}{R_2} + j\omega C_2\right)^{-1} = \frac{R_2}{j\omega C_2 R_2 + 1}$$

PRIMER:



$$Y_2 = G + j\omega C_2 = \frac{1}{R_2} + j\omega C_2$$

$$z_1 = R_1 + \frac{1}{j\omega C_1} = \frac{j\omega C_1 R_1 + 1}{j\omega C_1}$$

$$z_2 = 1/Y_2$$

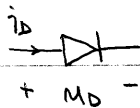
$$\Rightarrow A_u = - \frac{R_2}{\left(R_1 + \frac{1}{j\omega C_1}\right) (1 + j\omega C_2 R_2)}$$

$$A_u = - \frac{z_2}{z_1} = - \frac{R_2}{1 + j\omega C_2 R_2} \cdot \frac{j\omega C_1}{1 + j\omega C_1 R_1}$$

$$A_u = - \frac{z_2}{z_1} = - \frac{R_2}{j\omega C_2 R_2 + 1} \cdot \frac{j\omega C_1}{1 + j\omega C_1 R_1} = -$$

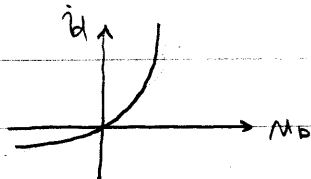
# Avditorne vaje

DIODA:

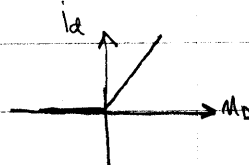


Dioda ... za negativne napetosti ne prevaja,  
za pozitivne pa predstavlja kratek stik:

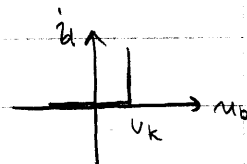
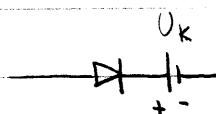
• Karakteristika idealne diode:



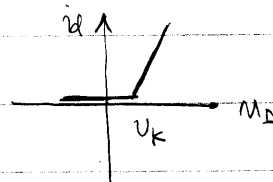
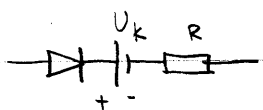
• Če tej idealni diodi dodamo upor dobimo naslednjo karakteristiko:



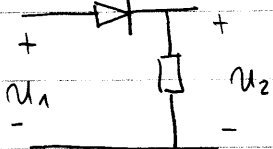
• Če diodi dodamo generator dobimo:



• Če diodi dodamo upor in generator dobimo:



1.) Imamo vezje:



a)  $U_1 > 0$  (če je dioda idealna)

→ kratek stik

⇒  $U_2 = U_1$

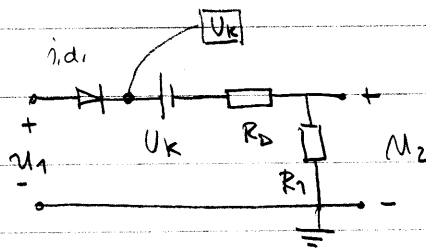


b)  $U_1 < 0$  (dioda in upor)



$U_2 > 0$  (→ odprta sponka)

2.) Zanima nas vpliv  $U_K$  in  $R_D$ :



a)  $U_1 > U_K$

b)  $U_1 < U_K$

→ k.s.

→ o.s.

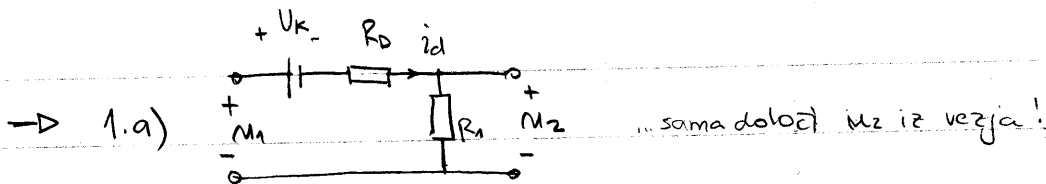
če želimo izvesti,

⇒  $U_2 = 0$

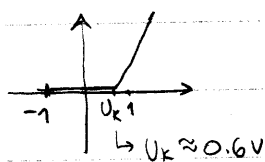
koliko je  $U_2$ , moramo

kvit vezje ⇒ na drugi strani

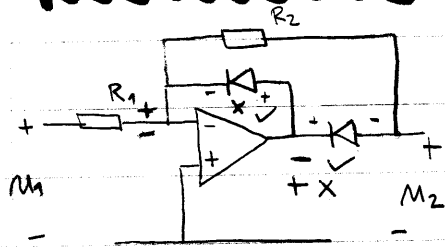




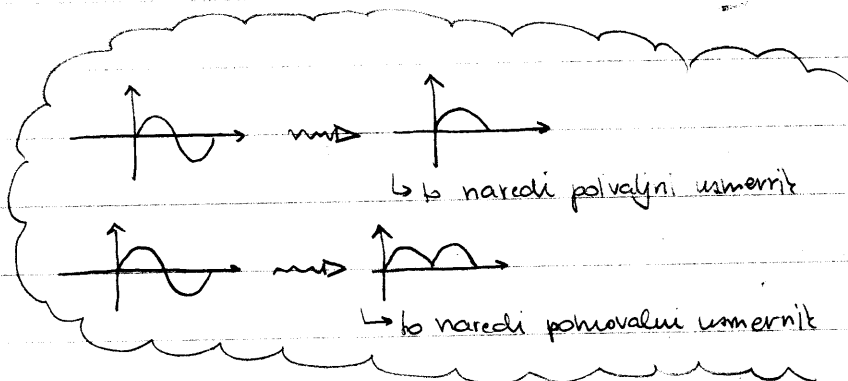
... dobimo:  $U_2 = \frac{(U_1 - U_k) R_1}{R_1 + R_D}$  ; če je  $R_1 \gg R_D$  potem:  $U_2 = U_1 - U_k$



**Polvalni usmernik:**



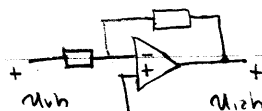
↳ imamo idealni diodi!



Ti dve diodi bosta vedno v kontra delovaju. Če bo ena prevajala bo druga zaprta...

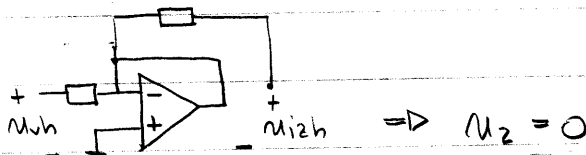
a)  $U_{vh} > 0$

• dobimo invertirajoč ojačevalnik:

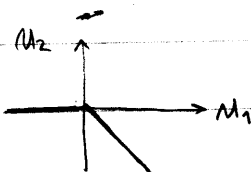


$\Rightarrow U_2 = U_{izh} = -\frac{R_2}{R_1} U_1 \mid_{R_1=R_2=R} = -U_1$

b)  $U_{vh} < 0$  ; dobimo:

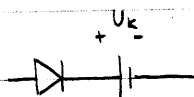


• Karakteristika polvalnega usmernika:



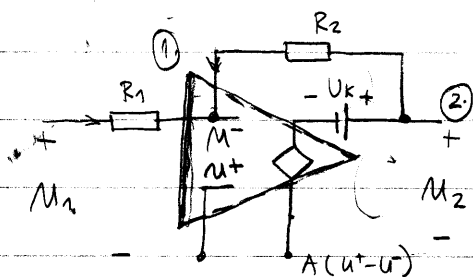
\* Če imamo [www.stromar.si](http://www.stromar.si)

poleg diode še generator:



- kako to vpliva na napetost na izhodu?

$U_{izh} = ?$



→ Vpliv kolena diode na vezje:

$$(1) \frac{U_1 - U^-}{R_1} + \frac{U_2 - U^-}{R_2} = 0$$

↳  $U^-$  (izrazimo)

$$(2) U_2 = A(U^+ - U^-) + U_k = -A \cdot U^- + U_k$$

↳ definicija

$$\frac{U_1}{R_1} + \frac{U_2}{R_2} - U^- \left( \frac{1}{R_1} + \frac{1}{R_2} \right) = 0$$

$$\Rightarrow U^- = \left( \frac{U_1}{R_1} + \frac{U_2}{R_2} \right) \cdot \frac{-R_1 \cdot R_2}{R_1 + R_2} = \frac{R_2 \cdot U_1 + R_1 \cdot U_2}{R_1 + R_2}$$

$$\Rightarrow U_2(U_1) = -A \cdot \frac{R_2 \cdot U_1 + R_1 \cdot U_2}{R_1 + R_2} + U_k$$

$$\Rightarrow U_2 = -A \frac{R_2 U_1}{R_1 + R_2} - A \frac{R_1 U_2}{R_1 + R_2} + U_k \rightarrow$$

$$\Rightarrow U_2 \left( 1 + \frac{A \cdot R_1}{R_1 + R_2} \right) = U_k - A \frac{R_2 U_1}{R_1 + R_2}$$

$$\Rightarrow U_2 \left( \frac{R_1 + R_2 + A \cdot R_1}{R_1 + R_2} \right) = \frac{U_k (R_1 + R_2) - A \cdot R_2 \cdot U_1}{R_1 + R_2} \Rightarrow U_2 = \frac{U_k (R_1 + R_2) - A \cdot R_2 \cdot U_1}{R_1 + R_2 + A \cdot R_1}$$

$$\Rightarrow U_2 = \frac{U_k (R_1 + R_2) - A \cdot R_2 \cdot U_1}{R_1 (1 + A) + R_2} = \frac{U_k (R_1 + R_2)}{R_1 + R_2 + A \cdot R_1} - \frac{A \cdot R_2 \cdot U_1}{R_1 + R_2 + A \cdot R_1}$$

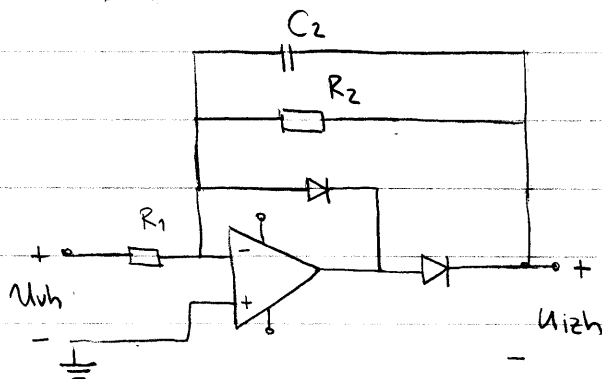
$$U_2 = - \frac{R_2}{R_1} U_1 + \frac{U_k (R_1 + R_2)}{A \cdot R_1} = \frac{U_k}{A}$$

če to upoštevamo  
dobimo  $U_2 \approx \frac{U_k}{A}$

$$\boxed{R_2 \rightarrow 0} \\ (1+A) \approx A$$

### Detektor srednje vrednosti:

→ enako kot polvalni usmernik, le da  
nima vezan še nek kondenzator  
(glej vajo 11!)



$$= \frac{U_k}{U_{sr}} \quad (\text{po času } T_1 \text{ je izh. nap.})$$

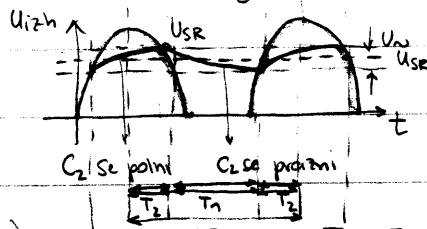
za  $U_k$  manjša od  $U_{sr}$ :  $\delta = \frac{U_k - U_{sr} \cdot 0.06}{U_{sr}} \dots$  glej list!

• če vezju polvalnega uarnernika dodamo kapacitivnost  $C$ , ki je vzporedno priključena k uporu  $R_2$ , dobimo detektor srednje vrednosti:

Naloga:

- Izračunajte  $C$  tako, da bo valovitost izh. nap. pri frekvenci 100 kHz približno 6% srednje vrednosti izhodnega signala:

$$g_e = \frac{U_{izh}}{U_{SR}} = 0,06$$



(noredili bomo dasti penastavitiv:

$$U_{izh} = U_{SR} = U_C = I_{SR} \cdot R_2 = R_2 \cdot \frac{1}{2\pi} \int_0^{\pi} \sin(\omega t) d\omega t = \frac{U_m}{\pi} \cdot \frac{R_2}{R_1}$$

$U_m$  ... amplituda vhodne napetosti

•  $T_2$  določimo s penastavitivjo, da je  $U_{izh}$  v točki  $T_2$  kar  $U_{SR}$

$$U_{izh} = U_{SR} = \frac{U_m}{\pi} \cdot \frac{R_2}{R_1} = U_m \cdot \frac{R_2}{R_1} \cdot \cos(\omega T_2) \Rightarrow \omega T_2 = \arccos\left(\frac{1}{\pi}\right) = \theta$$

$$T_2 = \frac{\theta}{180^\circ} \cdot \frac{\pi}{2\pi f} = \frac{\arccos\left(\frac{1}{\pi}\right)}{180^\circ \cdot 2 \cdot f} = 2 \text{ ms}$$

$$T = \frac{1}{f} = \frac{1}{100 \text{ Hz}} = 0,01 \text{ s} \Rightarrow T_1 = T - 2 \cdot T_2 = 6 \text{ ms}$$

$T_1$  ... čas praznjenja kondenzatorja

• Po času  $T_1$  je izh. napetost za  $U_C$  manjša od  $U_{SR}$ :

$$U_{SR} \cdot e^{-T_1/R_2 C_2} = U_{SR} (1 - g_e) \Rightarrow e^{-T_1/R_2 C_2} = (1 - g_e)$$

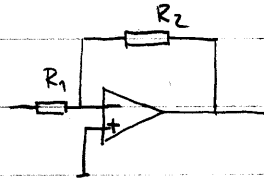
$$\Rightarrow \frac{T_1}{C_2 R_2} = \ln 0,94 \Rightarrow C_2 = \frac{T_1}{R_2 \cdot \ln 0,94} \approx 25 \mu\text{F}$$

•  $U_{izh} = \frac{U_m}{\pi} \cdot \frac{R_2}{R_1} = 1,24 \text{ V}$  ... izhodna napetost v okviru časovne analize

Avd. vaje ... 25.3.09

1.) <sup>ojačanje</sup> zanima nas izraz za invertirajočo o.j.:

$$A = -100; R_{vh} = 50k\Omega = R_1$$



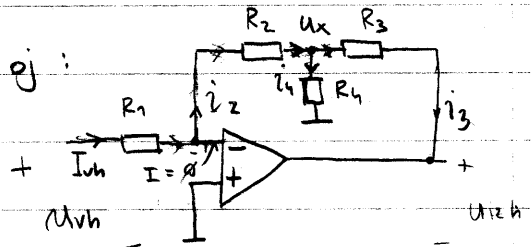
problem & da je to upor prevelik!

$$A = \frac{U_{izh}}{U_{vh}} = -\frac{R_2}{R_1} \Rightarrow R_2 = -R_1 \cdot A = -R_{vh} \cdot A = -50k\Omega \cdot (-100) = 5M\Omega$$

2.) Zanima nas izraz za ojačanje naslednjega o.j.:

$$A = \frac{U_{izh}}{U_{vh}} = -\frac{R_2}{R_1} R_3 \left( \frac{1}{R_2} + \frac{1}{R_3} + \frac{1}{R_4} \right)$$

$$A = -\frac{R_2}{R_1} \left( 1 + \frac{R_3}{R_2} + \frac{R_3}{R_4} \right)$$



to vezje ponaša resistor

Bomožne enačbe:

$$i_3 = (U_x - U_{izh}) \cdot \frac{1}{R_3}$$

$$U_x = 0 - i_2 R_2; i_{vh} = \frac{U_{vh}}{R_1} = i_2$$

$$i_2 = i_3 + i_4$$

$$i_4 = \frac{U_x}{R_4}$$

$$U_{vh} = i_2 \cdot R_1$$

$$i_2 = \frac{U_x - U_{izh}}{R_2} + \frac{U_x}{R_4}$$

$$-\frac{U_x}{R_2} = \frac{U_x}{R_3} - \frac{U_{izh}}{R_3} + \frac{U_x}{R_4}$$

$$\frac{U_{izh}}{R_3} = U_x \left( \frac{1}{R_2} + \frac{1}{R_3} + \frac{1}{R_4} \right)$$

$$\Rightarrow U_{izh} = U_x \left( \frac{1}{R_2} + \frac{1}{R_3} + \frac{1}{R_4} \right) \cdot R_3$$

$$U_x = -\frac{R_2}{R_1} \cdot U_{vh}$$

$$-\frac{U_{vh}}{R_1} = \frac{0 - U_x}{R_2} = 0$$

$$-U_{vh} \cdot \frac{R_2}{R_1} = U_x$$

$$i_2 - i_3 - i_4 = 0$$

$$i_2 = \frac{-U_x}{R_2}$$

$$\frac{U_x}{R_2} - \frac{U_x}{R_4} = \frac{U_x - U_{izh}}{R_3} \Rightarrow$$

$$U_{vh} = U_x \left( -\frac{R_1}{R_2} \right)$$

$$\frac{U_{vh}}{R_3} = U_x \left( \frac{1}{R_2} + \frac{1}{R_3} + \frac{1}{R_4} \right)$$

~ Torej lahko dobimo enak/podoben

invertirajočo ojačevalnik, tudi če

imamo dosti manjše upore.

npr.  $R_2 = 500k\Omega$

$R_3 = 500k\Omega$

$R_4 = 62,5k\Omega$

$R_1 = 50k\Omega$

...  $A = -100$  ... tako kot v 1. nalogi

$$U_2 = U_{vh1} \left(1 + \frac{R_2}{R_1}\right) - \frac{R_2}{R_1} U_{vh2}$$

$$U_3 = U_{vh2} \left(1 + \frac{R_2}{R_1}\right) - \frac{R_2}{R_1} U_{vh1}$$

### Instrumentacijski ojačevalnik

3.) Imamo vezje:

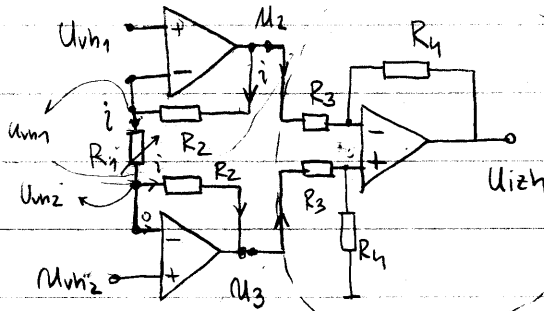
↳ to vezje uporabljamo,

kadar želimo ojačati

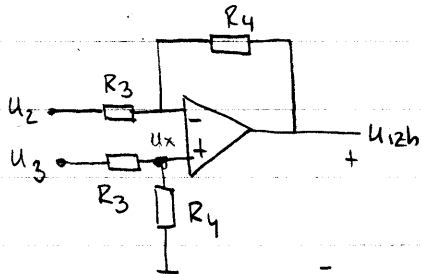
drobne diferencialne

vhode ... npr. tako

deluje ojačanje za EKG.



~ To rešimo tako, da pogledamo majhnej desni del vezja:



$$M_x = \frac{U_3 \cdot R_4}{R_3 + R_4}$$

$$U_{izh} = -\frac{R_4}{R_3} \cdot U_2 + \left(1 + \frac{R_4}{R_3}\right) \cdot \frac{R_4}{R_3 + R_4} \cdot U_3 =$$

$$U_{izh} = (U_3 - U_2) \frac{R_4}{R_3}$$

pride od invert. o.

ta del pride zaradi U<sub>3</sub>

~ Sedaj rešimo še levi del ojačevalnika:

$$i = \frac{U_{vh1} - U_{vh2}}{R_1}$$

reši do končnega izraza!

$$U_{izh1} = U_2, \quad U_{izh2} = U_3$$

?

?

$$U_{izh1} = U_2 = U_{vh1} + i R_2$$

$$U_{izh2} = U_3 = U_{vh2} - i R_2$$

Razmerje elementov mora biti enako:

$$\frac{R_4}{R_3} = \frac{R_2}{R_1}$$

Če je  $U_{vh1} = U_{vh2}$

$$\rightarrow U_{izh} = 0$$

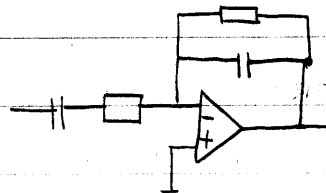
$$U_{izh1} = U_{vh1} + \frac{U_{vh1} - U_{vh2}}{R_1} R_2 = \dots$$

$$U_{izh2} = U_{vh2} - \frac{U_{vh1} - U_{vh2}}{R_1} R_2 = \dots$$

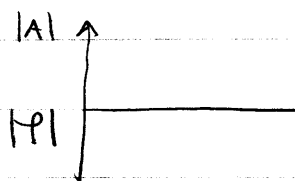
$$\Rightarrow U_{izh1} = \left(1 + \frac{R_2}{R_1}\right) U_{vh1} - \frac{R_2}{R_1} U_{vh2}$$

$$\Rightarrow U_{izh2} = \left(1 + \frac{R_2}{R_1}\right) U_{vh2} - \frac{R_2}{R_1} U_{vh1}$$

Analiza v frekvenanem prostoru:

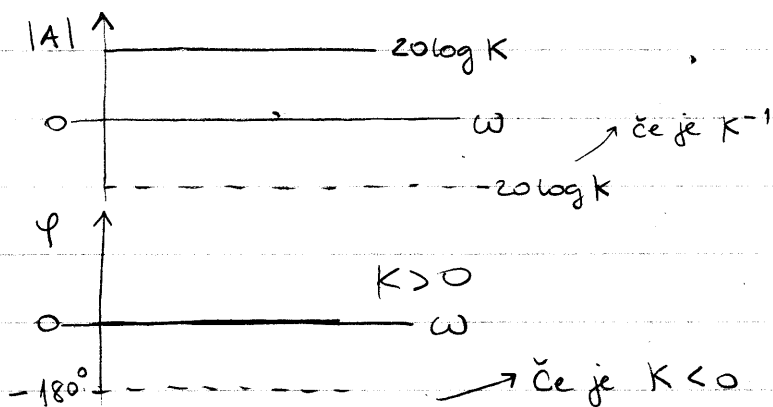


$$A(j\omega) = K \frac{j\omega(1 + j\omega/\omega_N)}{j\omega(1 + j\omega/\omega_P)}$$

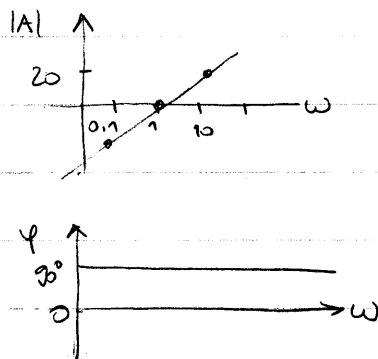


Bodejev diagram!

1. ~ Konstantno ojačanje:



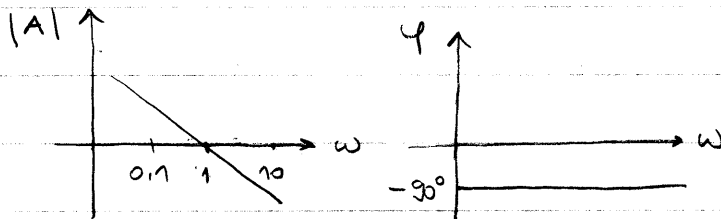
2. ~ Diferencialni o.j.:  $j\omega$



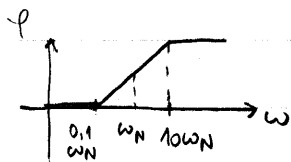
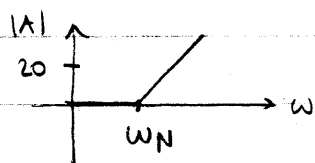
$$A(j\omega) = j\omega$$

3. ~ Integrirni element:

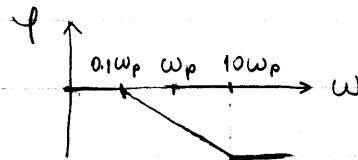
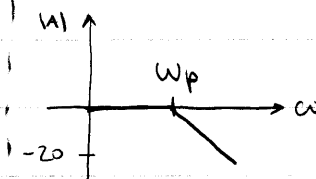
$$\frac{1}{j\omega}$$



4. ~ Ničla:  $(1 + \frac{j\omega}{\omega_N})$



5. ~ Pol:  $(1 + \frac{j\omega}{\omega_P})$

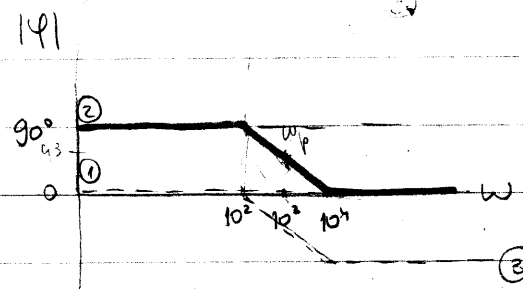
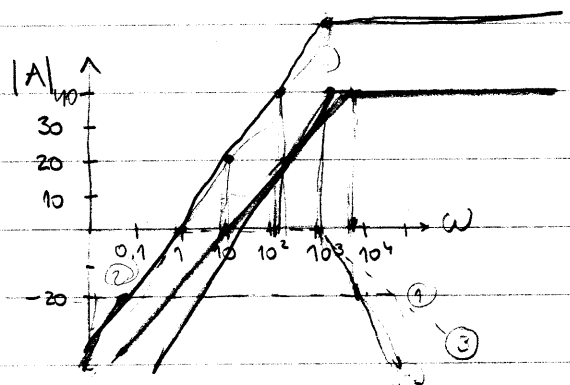


PRIMER:

$$A(j\omega) = \frac{0.1 j\omega}{1 + j\frac{\omega}{10^3}} \rightarrow$$

- potek amplitude
- potek faze

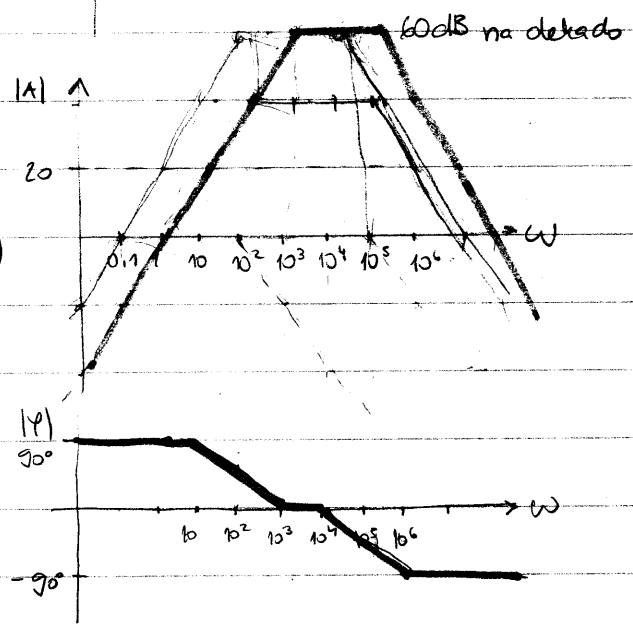
° Sešteti moramo ①, ② in ③



PRIMER:

$$A(j\omega) = \frac{10 j\omega}{(1 + j\frac{\omega}{10^2})(1 + j\frac{\omega}{10^5})}$$

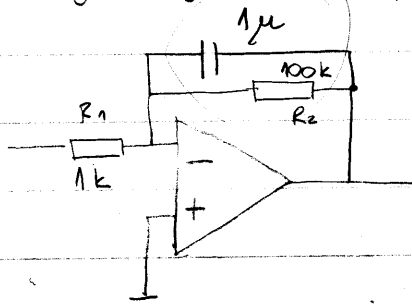
- amplitudni potek
- fazni potek



Domača naloga;  
 nariši bodejev diagram  
 za prejšnje primere (diferencialor / integrator)

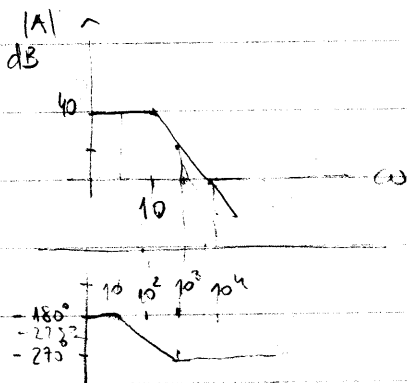
# Avditorne vaje:

1.) Nariši bodejev diagram za spodnji ojačevalnik



⇒ integrator

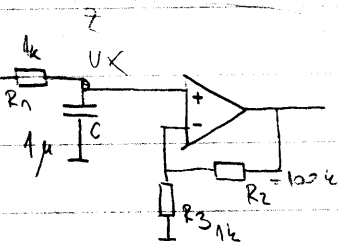
$$A_u = \frac{R_2/R_1}{1 + j\omega CR_2} = \frac{-100}{1 + j\omega/10}$$



$$\omega_p = 10$$

$$\left(j\omega C + \frac{1}{R_2}\right)^{-1} = \frac{R_2}{1 + j\omega CR_2}$$

2.) Nariši bodejev diagram za:



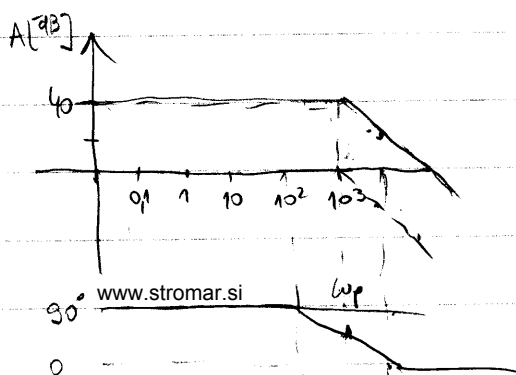
$$A = \left(1 + \frac{R_2}{R_3}\right) \frac{1}{j\omega R_1 C + 1} = \frac{101}{1 + j\omega/103}$$

$$\left(1 + \frac{R_2}{R_3}\right) U_x = U_{izh}$$

$$U_x = U_{uh} \cdot \frac{1}{j\omega C + R_1} = U_{uh} \frac{1}{1 + R_1 j\omega C}$$

$$U_{izh} = U_{uh} \left(1 + \frac{R_2}{R_3}\right) \left(\frac{1}{1 + R_1 j\omega C}\right)$$

$$A = \frac{101}{1 + \frac{j\omega}{103}}$$

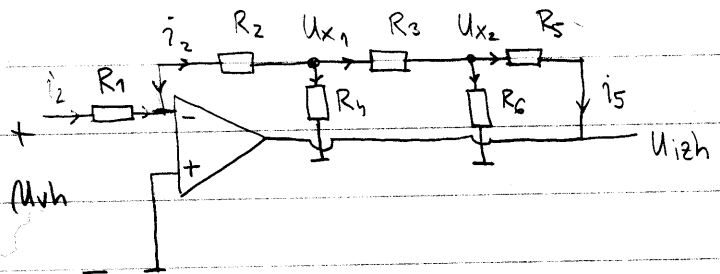
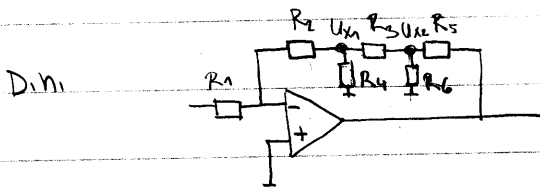




• Končni izraz za primer 3, ko imamo sestavljeno vezje iz treh o.j.:

$$\Rightarrow M_{izh} = \frac{R_1}{R_3} \left[ \left(1 + \frac{R_2}{R_1}\right) U_{vh2} - \frac{R_2}{R_1} U_{vh1} - \left(1 + \frac{R_2}{R_1}\right) U_{vh1} + \frac{R_2}{R_1} U_{vh2} \right] =$$

$$= \frac{R_1}{R_3} \left(1 + 2 \cdot \frac{R_2}{R_1}\right) (U_{vh2} - U_{vh1})$$



$$R_1 = R_2 = R_3 = R_4 = R_5 = R_6$$

$$i_2 = \dot{i}_2$$

$$i_3 = \dot{i}_5 + \dot{i}_6$$

$$\dot{i}_2 = -\frac{U_{x1}}{R_2}$$

$$\dot{i}_4 = \frac{U_{x1}}{R_4}$$

$$\dot{i}_6 = \frac{U_{x2}}{R_6}$$

$$\dot{i}_5 = \frac{U_{x2} - U_{izh}}{R_5}$$

$$\Rightarrow -\frac{U_{x1}}{R_2} = \frac{U_{x1}}{R_4} + \frac{U_{x2}}{R_6} + \frac{U_{x2}}{R_5} - \frac{U_{izh}}{R_5}$$

$$i_2 = i_4 + i_5 + i_6$$

$$\frac{M_{izh}}{R_5} = U_{x1} \left( \frac{1}{R_2} + \frac{1}{R_4} \right) + U_{x2} \left( \frac{1}{R_5} + \frac{1}{R_6} \right)$$

$$R_3 \cdot i_3 = U_{x1} - U_{x2} \Rightarrow U_{x2} = U_{x1} - R_3 (i_2 + i_4) = U_{x1} - R_3 \left( -\frac{U_{x1}}{R_2} + \frac{U_{x1}}{R_4} \right) =$$

$$i_3 + i_4 = i_2 \Rightarrow i_3 = i_2 - i_4 = U_{x1} \left( 1 + \frac{R_3}{R_2} + \frac{R_3}{R_4} \right)$$

$$\frac{M_{izh}}{R_5} = U_{x1} \left( \frac{1}{R_2} + \frac{1}{R_4} \right) + U_{x1} \left( 1 + \frac{R_3}{R_2} + \frac{R_3}{R_4} \right) \left( \frac{1}{R_5} + \frac{1}{R_6} \right) =$$

$$= U_{x1} \left( \frac{1}{R_2} + \frac{1}{R_4} + \frac{1}{R_5} + \frac{1}{R_6} + \frac{R_3}{R_2 R_5} + \frac{R_3}{R_2 R_6} + \frac{R_3}{R_4 R_5} + \frac{R_3}{R_4 R_6} \right) =$$

$$= U_{x1} \left( \dots \right)$$

$$\frac{U_{vh}}{R_1} + \frac{U_{x1}}{R_2} = 0 \Rightarrow U_{x2} = -\frac{R_2}{R_1} U_{vh}$$

$$M_{izh} = -\frac{R_5 \cdot R_2}{R_1} \cdot U_{vh} \left( \frac{1}{R_2} + \frac{1}{R_4} + \frac{1}{R_5} + \frac{1}{R_6} + R_3 \left( \frac{1}{R_2 R_5} + \frac{1}{R_2 R_6} + \frac{1}{R_4 R_5} + \frac{1}{R_4 R_6} \right) \right) =$$

$$= -8 \cdot U_{vh} \left( 4 \cdot \frac{1}{R} + 4 \cdot \frac{1}{R} \right) = -8 \cdot U_{vh} \rightarrow \frac{M_{izh}}{U_{vh}} = -8$$

2. način:

$$M^+ = M^- = 0$$

$$① \quad \frac{M_1 - M^-}{R} - \frac{M^- - U_{x1}}{R} = 0 \Rightarrow \frac{M_1}{R} + \frac{U_{x1}}{R} = 0$$

$$② \quad \frac{M^- - U_{x1}}{R} - \frac{U_{x1}}{R} - \frac{U_{x1} - U_{x2}}{R} = 0 \Rightarrow -\frac{U_{x1}}{R} - \frac{U_{x1}}{R} - \frac{U_{x1}}{R} + \frac{U_{x2}}{R} = 0$$

$$③ \quad \frac{U_{x1} - U_{x2}}{R} - \frac{U_{x2} - M_2}{R} - \frac{U_{x2}}{R} = 0 \Rightarrow \frac{U_{x1} - U_{x2}}{R} - \frac{U_{x2} - M_2}{R} - \frac{U_{x2}}{R} = 0$$

$$\Rightarrow M_1 = -U_{x1} \Rightarrow U_{x1} = -M_1$$

$$\Rightarrow -M_{x1} - U_{x1} - M_{x1} + U_{x2} = 0 \Rightarrow U_{x2} = 3U_{x1} = -3M_1$$

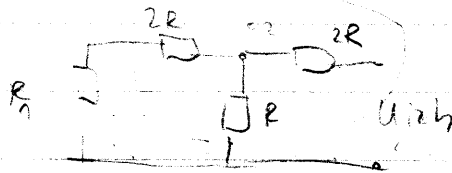
$$U_{x1} - U_{x2} - U_{x2} + M_2 - U_{x2} = 0 \Rightarrow -M_1 + 3M_1 + 3M_1 + M_2 + 3M_1 = 0$$

$$M_2 = -8M_1$$

$$A = -8$$

$$|A| = 8$$

$$M_{\text{sch}} = M_{\text{sch}} \cdot \left(1 + \frac{R_2}{R_1}\right)$$



$$R_2 = 2R \parallel R \parallel 2R = \left(\frac{1}{2R} + \frac{1}{R}\right)^{-1} \parallel 2R = \left(\frac{2R}{3} \parallel 2R = \left(\frac{3}{2R} + \frac{1}{2R}\right)^{-1}\right)$$

$$10R$$

$$M_{\text{sch}} = M_{\text{sch}} (1 + 10)$$

# DIGITALNA ELEKTRONIKA

UVOD:

Laboratorijska vaja: POLNI / POPOLNI SEŠTEVALNIK

zanimala nas bo implementacija (shematično)

videli bomo, da je shematičen način kar zamuden

↳ risanje vezja

Torej naša aritmetično-logična enota ALE bo opravljala

te funkcije sestavljanja

VHDL ... programski jezik ... visokonivojski jezik ... na dosti krajši način

bomo lahko naredili to, kar bomo delali celo 1. vajo

Ovladati moramo binarni zapis in linearno algebro !!

VDSL - modem (Iskratek)

Natisni si priručnik do naslednjice!

in ali ne  
AND, OR, NOT vrata



NAND in NOR



| x | y | x and y |
|---|---|---------|
| 0 | 0 | 0       |
| 0 | 1 | 0       |
| 1 | 0 | 0       |
| 1 | 1 | 1       |

| x | y | x or y |
|---|---|--------|
| 0 | 0 | 0      |
| 0 | 1 | 1      |
| 1 | 0 | 1      |
| 1 | 1 | 1      |

| x | not x |
|---|-------|
| 0 | 1     |
| 1 | 0     |

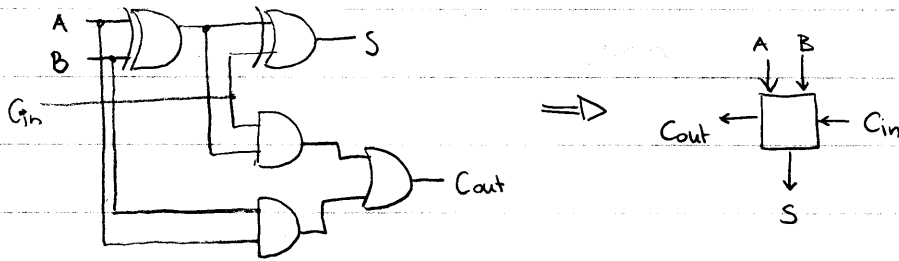
| x | y | x nand y |
|---|---|----------|
| 0 | 0 | 1        |
| 0 | 1 | 1        |
| 1 | 0 | 1        |
| 1 | 1 | 0        |

| x | y | x nor y |
|---|---|---------|
| 0 | 0 | 1       |
| 0 | 1 | 0       |
| 1 | 0 | 0       |
| 1 | 1 | 1       |

čipi: CPLD in FPGA

Popolni seštevalnik je vezje, ki ima dva vhode in prenos in izhod.

Realizacija na nivoju logičnih vrat:

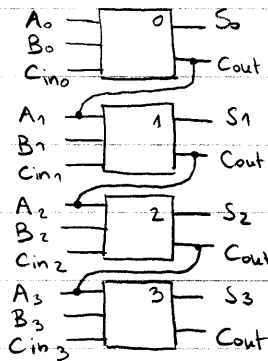


Imamo dve funkciji treh spremenljivk:  $C_{i+1} = AB + AC_i + BC_i$

TABELA

| A | B | C <sub>i</sub> | C <sub>i+1</sub> | S |
|---|---|----------------|------------------|---|
| 0 | 0 | 0              | 0                | 0 |
| 0 | 0 | 1              | 0                | 1 |
| 0 | 1 | 0              | 0                | 1 |
| 0 | 1 | 1              | 1                | 0 |
| 1 | 0 | 0              | 0                | 1 |
| 1 | 0 | 1              | 1                | 0 |
| 1 | 1 | 0              | 1                | 0 |
| 1 | 1 | 1              | 1                | 1 |

$$S = A \oplus B \oplus C_i = A \oplus (B \oplus C_i)$$



Manjka vaja - popolni seštevalnik! Realizacija v schematic - okolju

skripta!!!

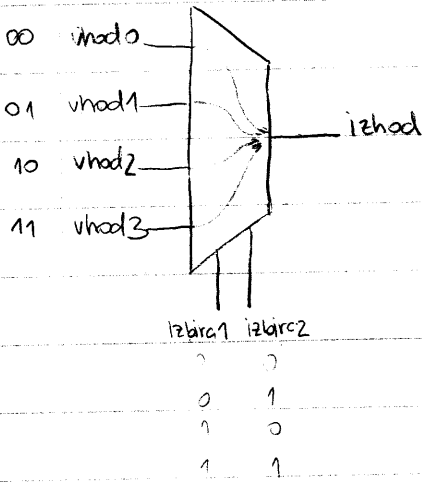
Logika

VHDL ukaz:  $izhod <= vhod0 \text{ when izbira} = '0' \text{ else vhod1};$

Sintakse se ne bomo naučili, ampak jo bomo skušali prebrati in glede na prejšnje primere riniti než nov primer.

1. PRIMER (Kako se realizira MUX?) - glej PDF-je

(new project, → add file ... odprl in poskušaj razumeti!)



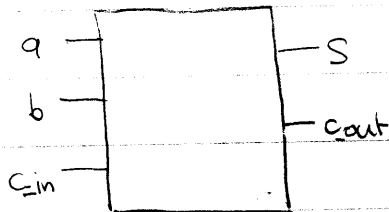
- Nariši vezje na njegju vrat za ta primer:

D.n.

- Kako bi to realizirali z VHDL?

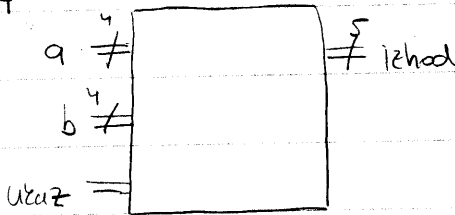
(→ z vektorji si mnogo lažje predstavljamo signale...)

Popolni sestevalnik



... To vezje imamo na računalniku, ga odberemo in realiziramo... Komentar:

ALE4



Zakaj imamo na izhodu 5 bitov, če imamo 4-bitne vhode? Odgovor je v sestevanju dveh štiribitnih (n-bitnih) števk ... rezultat je (n+1)-bitni. V našem primeru 5-bitni!

(. če pri negativnih številih sprava delujemo enako nič ne spremeni, če pri

① Dekodirnik : Navodila - VHDL - projekt 1 → CPLD

↳ treba ga je nadgraditi



\* ncf datoteka

1 del

koraki implementacije

② Navodila - VHDL - projekt 2 sistem na podlagi 1. dela

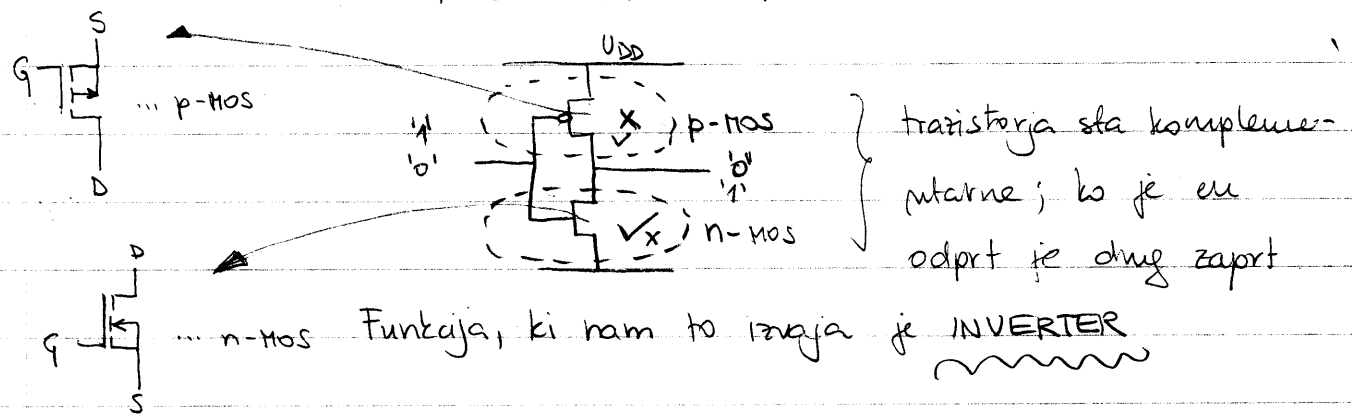
Razmisli o razliki med programiranjem mikroprocesorjev in VHDL!

→ pomeni komplementaren

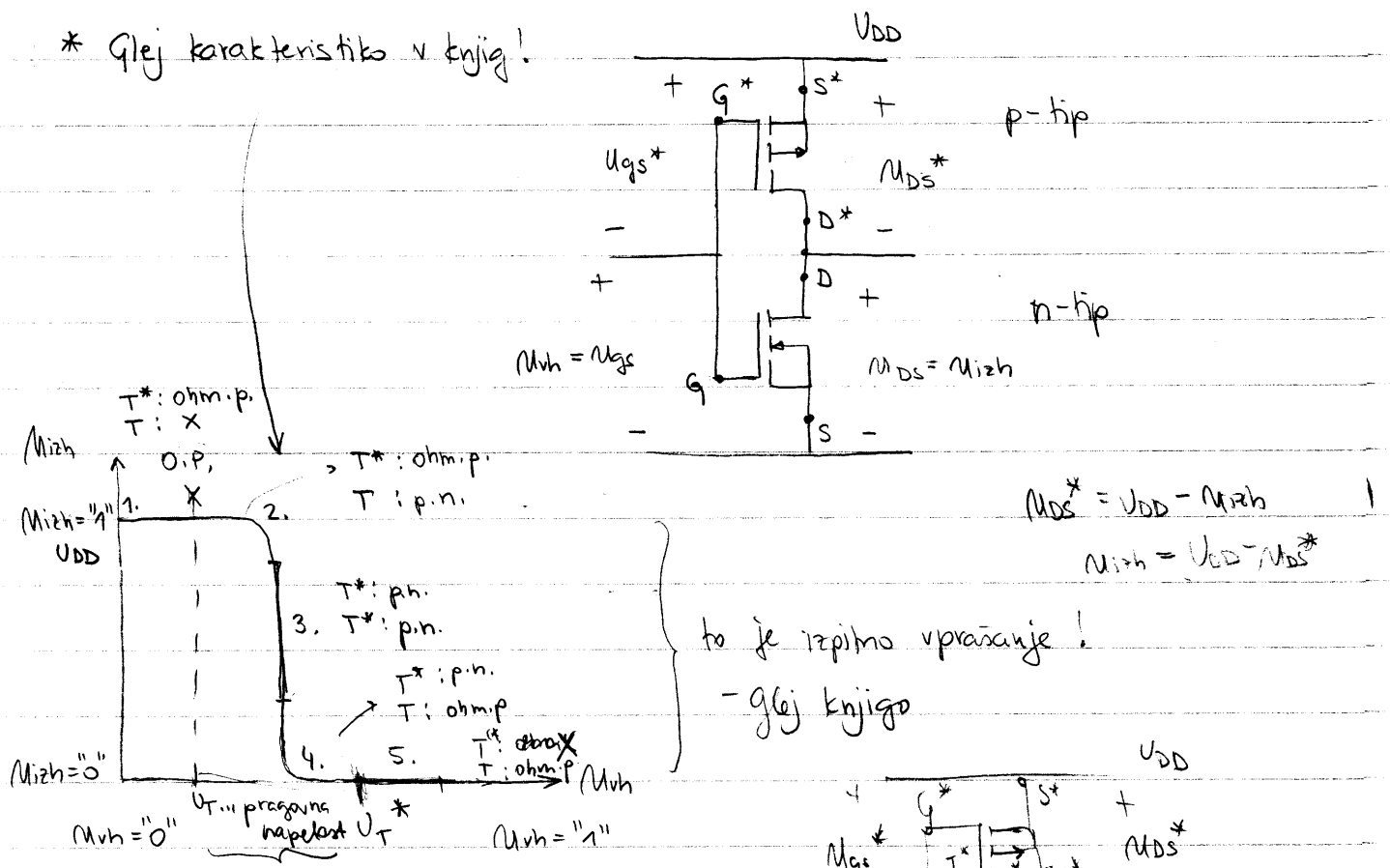
# C-MOS TRANZISTOR

izpitna naloga: OSNOVNO DELOVANJE C-MOS TRANZISTORJA:

\* C-MOS INVERTER IZGLEDA TAKOLE:

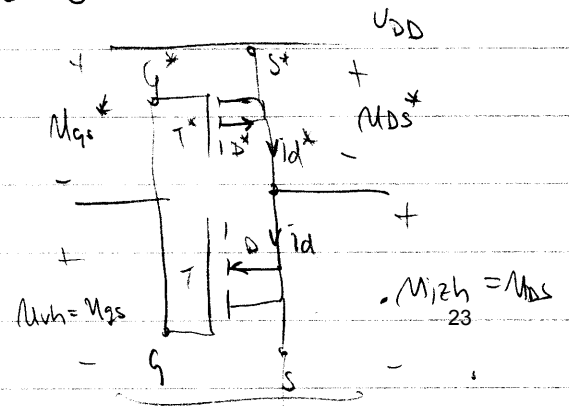


\* Glej karakteristiko v knjigi!



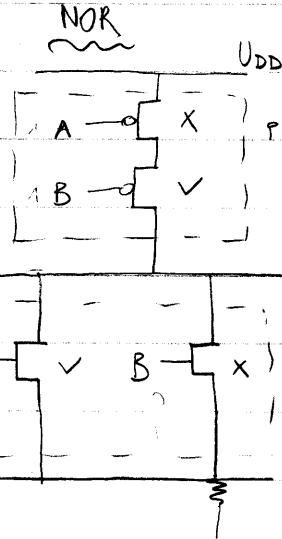
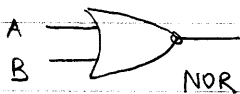
to je izpitna vprašanje!  
- glej knjigo

naš prehod je "hiter" in simetričen



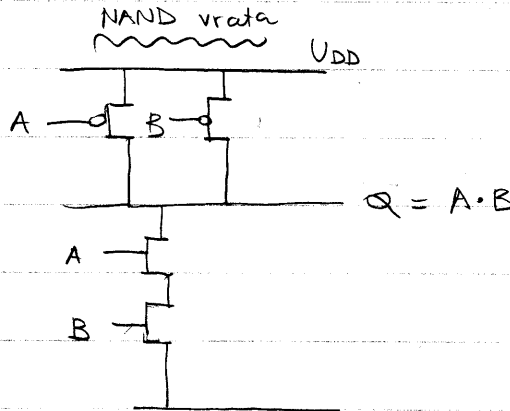
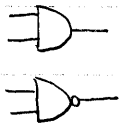
Tri Booleve operacije: in, ali, ne. Pri CMOSu nas zanima kako realiziramo in in ali vrata.

| A | B | OR | NOR |
|---|---|----|-----|
| 0 | 0 | 0  | 1   |
| 0 | 1 | 1  | 0   |
| 1 | 0 | 1  | 0   |
| 1 | 1 | 1  | 0   |



Vsaka aline (NOR) vrata potrebuje 4 tranzistorjev

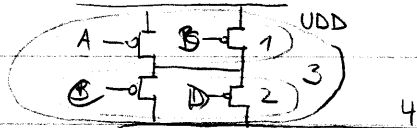
| A | B | NAND | AND |
|---|---|------|-----|
| 0 | 0 | 1    | 0   |
| 0 | 1 | 1    | 0   |
| 1 | 0 | 1    | 0   |
| 1 | 1 | 0    | 1   |



$$\overline{Q} = A \cdot B =$$

$$Q = \overline{A + B}$$

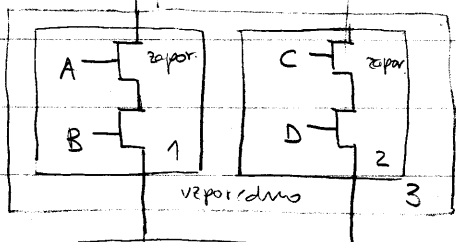
Realizacija



Dvigna in vrata vezana z ali vrati:

Kaj je na izhodu, če 10 na vhodu same črte ali same pule

↳ na izhodu je 0      ↳ na izhodu je 1

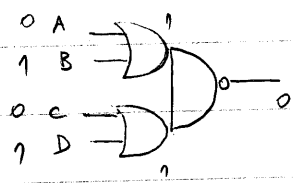


Vse kar smo spodaj narisali zaporedno je zgoraj obratno!

$$\overline{Q} = A \cdot B + C \cdot D$$



• Še ena realizacija za to je:



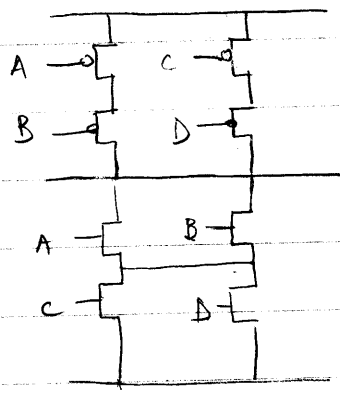
Dvojna ali vrata vezana

z ne vrati.

(napišemo najprej spodnji del in potem zgoraj vse komplementarno)

↳ Moras znati narisati

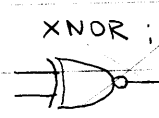
realizacijo in preveriti, če dela!



$$\bar{Q} = (A+B)(C+D)$$

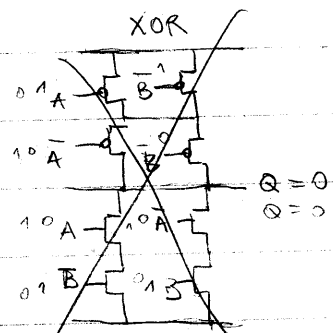
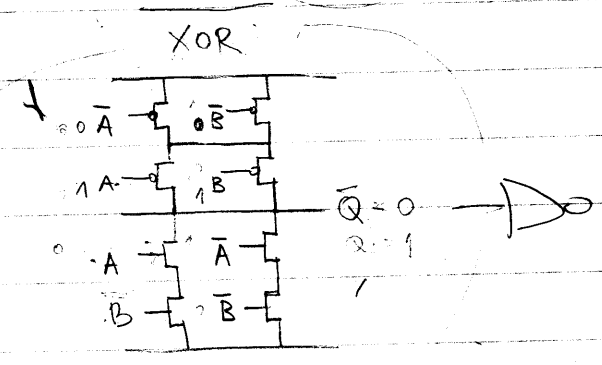
• Zanima nas ~~X~~ XOR funkcija in

XOR:  $\bar{Q} = \bar{x}y + x\bar{y}$



XNOR:  $\bar{Q} = xy + \bar{x}\bar{y}$

| x | y | $\bar{x}$ | $\bar{y}$ | $x\bar{y}$ | $\bar{x}y$ | XOR | XNOR |
|---|---|-----------|-----------|------------|------------|-----|------|
| 0 | 0 | 1         | 1         | 0          | 0          | 0   | 1    |
| 0 | 1 | 1         | 0         | 0          | 1          | 1   | 0    |
| 1 | 0 | 0         | 1         | 1          | 0          | 1   | 0    |
| 1 | 1 | 0         | 0         | 0          | 1          | 0   | 1    |



$$\bar{Q} = xy + \bar{x}\bar{y}$$

$$\overline{xy + \bar{x}\bar{y}} = (\bar{x} + \bar{y})(x + y) =$$

$$Q = \bar{x}y + x\bar{y}$$

$$\bar{x}y + x\bar{y} = f_{xor} = f_{xnor} = \overline{xy + \bar{x}\bar{y}}$$

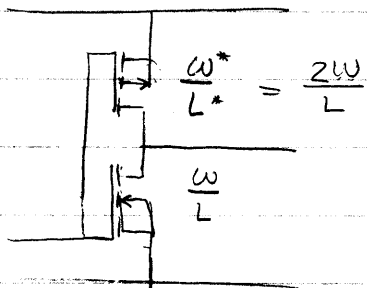
Simetrična prelopná karakteristika: p-tip pišemo z zvezdico!

Še neki si pogledjmo: Dimenziranje tranzistorjev: dolžina kanala napram

Širini kanale:

Spomnimo se konstante:  $k = \frac{\mu_n \cdot C_{ox} \cdot W}{2L}$ ;  $k^* = \frac{\mu_p \cdot C_{ox} \cdot W^*}{2L^*}$

$\mu_p \approx 2 \mu_n$  (mobilnost protonov je do 3x 2x mobilnost elektronov)

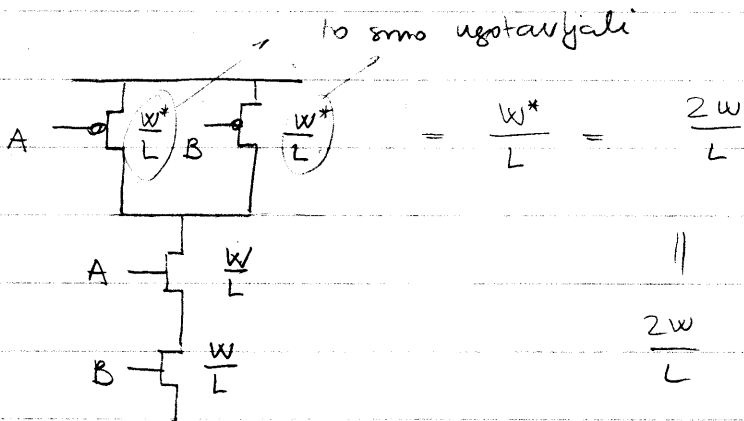


$L = L^*$

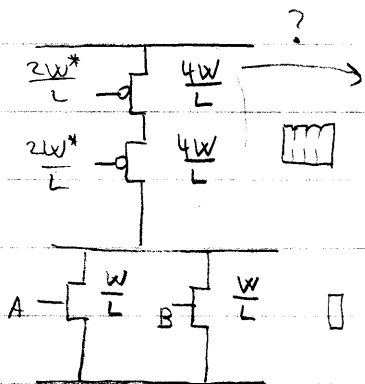
$W = \frac{W^*}{2}$

$W^* = 2W$

PRIMER:



PRIMER:



faktor 2 pride zaradi manjše mobilnosti  
in še faktor 2 zaradi dolžine kanala

Izpit:

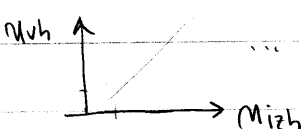
- pravilne karakteristike nekake vezje
- kakega diagrama
- polvalni poskusniki
- VHDL (A, B, C, D) - skripta na netu

## ZADNJE VAJE - Povzetek celega leta

$\mu p$

- Kakšna je razlika med mikroprocesorji in FPGA-ji?
- $\mu p$  in FPGA-ji realizirajo pretvorbo signala in analognega v digitalnega
- Zmožnejši FPGA-ji imajo znotraj sebe še en  $\mu p$ ...
- Spoznali smo VHDL za programiranje čipov?

① Operacijski gočevalnik  
- časovni ( $\mu s$ )



... to je prenosna karakteristika

② Instrumentacijski gočevalnik - polvalni memorizir

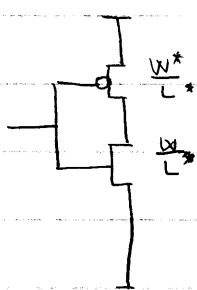
③ Bodejev diagram (dal bo neto konstulacijo, pa bo treba pun razbrat izraz npr.  $z_2/z_1$  in pol narisat)

④ VHDL - osnovni gradniki

⑤ C-MOS - teorija iz knjige !!!  
- realizacija funkcije na c-mosu  
-  $w/L$

ata  
morno znat povedat vati o delovanju  
poudarek na c-mos inverziji

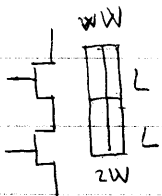
C-MOS



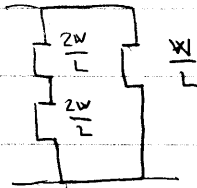
- manjša mobilnost

pri isti dolžini  $L=L^*$

je  $W^* = 2W$

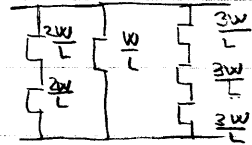


pri serijski vezavi rabim 2x širši



zaporedne vezava

n-tip



to je zdej najslabša pot!

to pomeni tri vhodne vrata



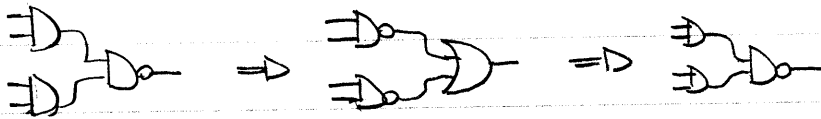
n-tip

zaporedne vezava - nima mo teli težav

~ gledam najslabši primer (če imam več poti gledam tisto, ker rabim največ) ... gledam najdaljšo pot (največ L)

To je koncept, naprej pa poglej doma - ker bo to na izpitu

Kako iz logičnih vrat sestaviš tranzistorško vezavo, Znamo  
sestaviti poljubno kombinacijsko prenegetivnih vrat, ki so ne le ena  
prevezava z negiranimi vrati... treba je malo naredit tudi obratno



glej deMorgana!